PRE. Ambt #

-1-

Docket: 0756-2101

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re New PATENT Application of			
Shunpei YAMAZAKI et al.			
Serial No. Not Yet Assigned) Attn: Applications	
Filed: February 11, 2000)	Branch
For:	SEMICONDUCTOR DEIVCE)	
	AND METHOD OF)	
	MANUFACTURING THEREFOR)Dat	te: February 11, 2000
	PRELIMINARY AME	NDME	ENT
Hono	rable Assistant Commissioner for Patent	S	
Wash	nington, D.C. 20231		
Sir:			

Please preliminarily amend the subject application as follows:

IN THE SPECIFICATION:

Page 12, after line 6, insert the following description:

--Fig. 27 is a graph of gate voltage (VG) versus drain current (ID).

